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501.41263X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): NAKABAYASHI et al

Serial No.: 10/091,489

Filed: March 7, 2002

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Group: 2818

Examiner: Le

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 14, 2004

Sir:

In response to the Office Action mailed October 20, 2003, please amend the above-identified application as set forth herein. This paper includes the following items:

**Amendment to the Claims; and**

**Remarks.**